OUTPUTS	Frequency	Level (into 500)								
Output	Frequency	Level (into 50Ω)								
A	10 MHz	+13 ±1.5 dBm								
В	100 MHz	+20 ±1.5 dBm								
VSWR										
< 1.5:1,										
STABILITY	,									
Aging										
	first year									
	days operating,									
5 x 10 ⁻⁸	second year, ty	pical								
3×10^{-8} per year thereafter, typical										
Phase Noise L(f), dBc/Hz										
	10 MHz	100 MHz								
1 Hz	-110	-86								
10 Hz	-140	-116								
100 Hz	-162	-137								
300 Hz	-167	-137								
1 kHz	-172	-151								
10 kHz	-174	-172								
100 kHz	-174	-174								
1 MHz ≥10 MHz	-174 -174	-175 -175								
	re Stability	-1/5								
	⁻⁸ , 0 to +50°C (
		Ref. +25°C)								
Harmonics										
≤ -25 dBc Sub-Harmonics										
Sub-narmonics ≤ -70 dBc										
PLL Reference Products										
≤ -70 dl	Вс									
Spurious										
\leq -80 dBc, excluding power										
supply line related spurs										
Phase Lock Alarm										
Locked: +3.5 VDC to +5.2 VDC (Hi) Out-of-Lock: +0.8 VDC max (Lo)										
Phase Lock Voltage Monitor										
Voltage monitor pin supplied										
MECHANICAL										
Dimensior	าร									
5.56 x 4	4 x 1″									
Connector	ſS									
	outs: SMA(f)									
		ed Thru Terminals								
	round Turret									
Packaging										
	plated machine									
	im housing – J	INMX								
Mounting	od incorto on to									
	ed inserts on ba 9 places	15C,								
#Z-30,	5 places									

DOWED DEQUIDEMENTS		REV	DATE		REVISION RECORD		DWN	AUTH		
POWER REQUIREMENTS Warm-Up Power		- 04-11-13		Initial Release			Liz			
\leq 17 Watts for 5 minutes	·	А	10-29-15	Updated Drav	ving		PAC			
Total Power										
≤ 10 Watts at +25°C										
Supply Voltage										
+15 VDC ±5%	-									
ADJUSTMENT			11	PMX MXO Connecti	ions					
Mechanical Tuning (Internal 10 MHz)			Connector	Function						
$\pm 1 \times 10^{-6}$			1	Supply Voltage						
Electrical Tuning			2	Ground, Case Electrical Tuning	_					
$\pm 2 \times 10^{-7}, \pm 5 \text{ VDC}$			4	RF Output B	-					
Negative Slope			5	Phase Lock Volt Phase Lock Alar						
Loop BW (Internal 100 MHz PLL)			8	RF Output A N/C						
Target Bandwidth: ~250 Hz				N/C						
Type 2 Loop										
CRYSTAL	1.00 — 2X 0.82 —	9 🔘 🔇	3		O 1	9				
Туре			<i>v</i> -)				
10 MHz SC-cut	0.49 — 0.44 —				© 2	4				
100 MHz SC-cut	0 — L									
ENVIRONMENT		9 5 9	2	6 6	22	91				
Operating Temperature 0 to +50°C		0.40	0	2.49	3.55	5.01				
Storage Temperature			h	μщ	# 5	긎				
-50 to +85°C	4.00 — 3.915 J	/®	8 8		Ĵ [™] ® ® [™]	9 ®		1000 - 100 - 100		
Relative Humidity	5.515		0	8		ø	h			
Up to 65%, non-condensing, operating				6	• •		l			
OTHER	8		8	ø 6	» «	8	ĺ			
Label							>			
Use conventional label with the	8	1	8	ø 6		8				
following information:	2.000 — C	n .	-		-	0				
501-26745 (Current Rev.)	1.750 —		•				7			
10M/100M MXO-PLMX +15 VDC	6		8	8	9 ®	8				
Serial # - Date Code							Ŷ			
(Mark connectors with function)	8	1	0	ø 6	8	8	,			
Test Data		Thread	led Inserts, # 2-56,							
- Output Level	0.085 -		laces, 0.15" deep	ø 6		9	ų			
- Phase Noise	0 - 0		8 8		୬ ଛ	8 ⁄0				
- Temperature Stability	-	ы С П		ν 	n	۲ ٥	0	- 92		
- Harmonics, Subs, Products, Spurs		0.085 0.395		2.275		5.470		0.76		
 Power – Warm-up and Total 										
	-									
	Wenzel Associates, Inc.									
	Austin, Texas									
	Title:									
	10/100 MHz Ultra Low Noise Crystal Oscillator (MXO-PLMX)									
		P/N:		Rev:	Date:	Drawn:	Re	ef:		
			1-26745		10-29-15			26045		
						5001/				
		Tolerances (except as i	noted)	0.XX Dec:	0.XXX Dec:	FSCM: 62821	Page 1	of 1		
		Dimensions	are in inches	±0.030"	±0.010"	02021	i aye I (