

# **DP-PSU-7059**

# **Power Distribution Module**

# **KEY FEATURES AND BENEFITS**

- 53 current limiter switches with ON/OFF control
- Programmable current limit with inrush current control timer
- 80 channels of ADC for voltage and current monitoring
- Command and control from OBC and Tele command modules
- Dual hardware redundant FPGAs for reliable operations

#### **APPLICATIONS**

- Power system for Low Earth Orbit (LEO) Satellites
- Micro/Nano satellite system bus
- Power system sequencer applications
- Various current monitoring and control applications



#### DESCRIPTION

The Power distribution module (PDM), controls the flow of power to spacecraft sub systems / instruments and is custom designed based on specific spacecraft power requirements. The regulated output voltage from EPS (Electrical Power System) is fed to the PDM where the supply voltages are switched through the current limiter MOSFET switches and made available to the sub systems. These individual switches are controlled through Tele command operation or on board autonomous operation for load management.

The PDM also monitors the output from battery, solar power sources and regulated bus voltages of EPS. In case of availability of two power sources in the satellite system, the module can be programmed to perform automatic switching between the main and redundant EPS systems based on the power availability and module health status. This module is equipped with 2 channels of I<sup>2</sup>C interface and 2 channel SPI (Serial Peripheral Interface) for easy integration with other sub systems like OBC, EPS and Tele command system. The base module contains 17 Latching current limiters. A pluggable daughter module is an expander module which offers additional 36 nos. of current limiter switches for flexible system integration.

#### **BLOCK LEVEL EXPLANATION**

### **CURRENT LIMIT SWITCH**

The PDM consists of 53 nos. of latching current limiters. The current limits can be programmed through hardware control. Each current limiter switch powers the individual subsystems in response to a telecommand. These switches shut off automatically in the event of an overload or short-cut condition and also limit the inrush current making these suitable for capacitive loads. An internal reverse-voltage detection comparator disables the power-switch to protect devices on the input side of the switch in the event that the output voltage is driven higher than the input.

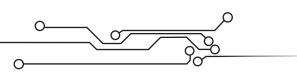
#### ANALOG TO DIGITAL INTERFACE (ADC)

The PDM has 80 ADC channels to monitor the bus voltages, battery cell voltage and load current through each current limiter switches. The monitored ADC data in 10 / 12 bit resolution can be sent over telemetry for ground observation.

# FIELD PROGRAMMABLE GATE ARRAY (FPGA)

The module consists of two programmable gate array devices, which operate in parallel as backup for each other. The parallel redundant system refers to resilience that ensures system availability in the event of component failure and continues to supply uninterrupted power to the critical loads. These FPGAs along with I/O expander are used to control the sub module ON/OFF operation based on telecommanding.





**DP-PSU-7059** 

## **POWER DISTRIBUTION MODULE - EXPANDER**



# **SPECIFICATIONS**

Type: Power distribution module

#### **INPUT AND OUTPUT SPECIFICATIONS**

### Input

Channel 1 : Input Supply : 3.3V to 12V at 4A max
Channel 2 : Input Supply : 3.3V to 12V at 4A max
Channel 3 : Input Supply : 3.3V to 12V at 4A max
: Input Supply : 3.3V to 12V at 4A max

#### Output

Channel 1 distribution : Main Board :5 nos switches Expander Board : 7nos

Channel 2 distribution: Main Board: 7 nos

switches

Expander Board: 9nos

Channel 3 distribution: Main Board: 5 nos

switches Expander Board : 20nos

### **POWER**

Power consumption : Less than or equal to 1W

### **COMMUNICATION INTERFACES**

- Two SPI channels from each FPGA
- $\bullet \ \, \text{Two} \ \, \text{I}^2\text{C slave channels from each FPGA}$

# CONNECTORS

Input / Output connector : 84 Pin Nicomatic sub connector

JTAG interface connector : 5X2 Header

Board to board interface : 10X2 Samtec connector

connector

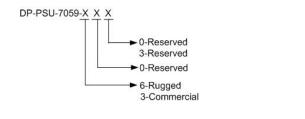
### **ENVIRONMENTAL**

Operating temperature : -15°C to 55°C Storage temperature : -40°C to 58°C Commercial or rugged : Rugged

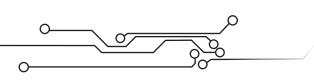
#### **MECHANICAL**

Dimension in mm (LXBXH) : 96 x 90x 16 Weight in grams : <90

# ORDERING INFORMATION







**DP-PSU-7059** 

### **BLOCK DIAGRAM OF DP-PSU-7059** 5VDC\_M Input Supply Bus EPS & Battery Module 1 w • 3.3VDC\_M Current Monitoring\_1 MOSFET DRIVER ON / OFF Control\_ Raw Bus voltage\_M Ourent limit switch ON/ 5VDC\_R Current Limited switch 33 INTERFACE CONNECTOR **EPS & Battery** Module 2 Input Supply Bus ON / OFF Control\_33 Raw Bus voltage\_R INPUT/OUTPUT INTERFACE CONNECTOR Current Limited Supply\_1 Modules & Sensors Current Limited Supply\_16 IO\_EXPANDER\_FPGAL\_SPL\_BUS IO\_EXPANDER\_FPGAT\_SPI\_BUS Buffer **FPGA** Communication (Main) OBC\_I2C\_FPGA1 **FPGA** OBC\_I2C\_FPGA2 (Redundant) BUFFER Tele Cond RX-1 SPI Tele Cmd RX-1 Tele Crid RX-2 SPI ADC 4 (#16 Channels) Tele Crnd RX-2 FET Buffer BUS ADC 5 FET Buffer EPS 1 Voltage monitoring (#16 Channels)

#### Copyright 2016 by Data Patterns (India) Pvt. Ltd.

All rights reserved. None of the materials provided in this Data Sheet may be used, reproduced or transmitted, in whole or in part, in any form or by any means, electronic or mechanical, including photocopying, recording or the use of any information storage and retrieval system.

DATA PATTERNS (INDIA) PRIVATE LIMITED, No.19, Arya Gowder Road, West Mambalam, Chennai - 600 033, INDIA Tel: 24837460, 24848703 | Fax: 91-44-4741 4444 | Email: marketing@datapatterns.co.in Website: www.datapatternsindia.com

