

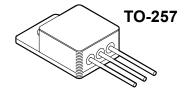
RAD HARD POSITIVE, 3 AMP, LOW DROPOUT ADJ VOLTAGE REGULATOR

5971RH

FEATURES:

- Total Dose Hardened to 100 Krad(Si) (Method 1019.7 Condition A)
- · Low Dropout Voltage
- · Internal Short Circuit Current Limit
- · Output Voltage is Adjustable with 2 External Resistors
- Output Current Capability to 3 Amps
- Internal Thermal Overload Protection
- Available in 3 Lead Form Options: Straight, Up and Down (TO257)
- Available to DSCC SMD 5962R09212

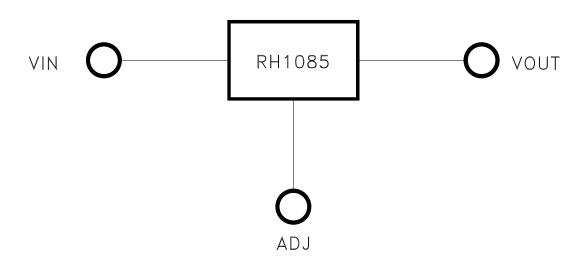




DESCRIPTION:

The MSK 5971RH offers low dropout voltage while offering radiation tolerance for space applications. This, combined with the low θ JC, allows increased output current while providing exceptional device efficiency. Because of the increased efficiency, a small hermetic 3 pin package either SMD-1 or TO-257, can be used providing maximum performance while occupying minimal board space. Output voltage is selected by the user through the use of 2 external resistors. Additionally, the regulator offers internal short circuit current and thermal limiting, which allows circuit protection and eliminates the need for external components and excessive derating. The MSK 5971RH is available in two space saving package options; the 3 pin power surface mount SMD-1 and the TO-257 package with 3 lead form options: straight, up and down.

EQUIVALENT SCHEMATIC



TYPICAL APPLICATIONS

- · High Efficiency Linear Regulators
- Constant Voltage/Current Regulators
- · System Power Supplies
- · Switching Power Supply Post Regulators

PIN-OUT INFORMATION

| D-257 | SMD-1 | | | |
|--------------|-------------|-----------------|--|--|
| ADJ | 1 | ADJ | | |
| VOUT | 2 | VIN | | |
| VIN | 3 | VOUT | | |
| | ADJ VOUT | ADJ 1 VOUT 2 | | |

ABSOLUTE MAXIMUM RATINGS

(8)

| ±VIN | Input Voltage (WRT VOUT)30V | TsT Storage Temperature Range65°C to +150°C |
|------|--------------------------------------|---|
| Pp | Power Dissipation Internally Limited | TLD Lead Temperature Range |
| lout | Output Current(7) 3A | (10 Seconds)300°C |
| TJ | Junction Temperature+150°C | Tc Case Operating Temperature |
| | | MSK5971RH40°C to +85°C |

ELECTRICAL SPECIFICATIONS

| Parameter | Test Conditions ① ③ ⑨ | | Group A | MSK5971K/H RH | | | MSK5971RH | | | Units |
|---------------------------|---|----------------|----------|---------------|-------|-------|-----------|-------|-------|--------|
| Farameter | | | Subgroup | Min. | Тур. | Max. | Min. | Тур. | Max. | Ullits |
| Reference Voltage | IOUT = 10mA VIN = VOUT+3V | 1 | 1.238 | 1.250 | 1.262 | 1.225 | 1.250 | 1.270 | V | |
| | | 2,3 | 1.225 | 1.250 | 1.270 | - | - | - | V | |
| | | Post Radiation | 1 | 1.205 | 1.24 | 1.262 | 1.205 | 1.224 | 1.270 | V |
| Dropout Voltage | $10mA \leq IOUT \leq 3A \Delta V$ | OUT = 1% | 1 | - | 1.3 | 1.65 | - | 1.3 | 1.65 | V |
| Line Regulation | Iout=10mA | | 1 | -0.5 | 0.1 | 0.5 | - | 0.1 | 0.6 | % |
| | $(VOUT+3V) \le VIN \le (VOUT +15V)$ | | 2,3 | -0.75 | 0.2 | 0.75 | - | - | - | % |
| Load Regulation | VIN = VOUT +3V | | 1 | -1.0 | 0.2 | 1.0 | - | 0.2 | 2.0 | % |
| | $10mA \leq IOUT \leq 3A$ | | 2,3 | -2.0 | 0.3 | 2.0 | - | - | - | % |
| Short Circuit Current (7) | VIN = VOUT + | +5 | 1 | 3.0 | 4.2 | - | 3.0 | 4.2 | - | Α |
| Short Circuit Current (7) | | Post Radiation | 1 | 3.0 | 4.1 | - | 3.0 | 4.1 | - | Α |
| Ripple Rejection ② | Ιουτ = 3A; Cουτ = 25μF | ; f = 120Hz | - | - | 75 | - | - | 75 | - | dB |
| Thermal Resistance ② | Junction to Case @ 125°C TO-257 Package | | - | - | 3.0 | 3.5 | - | 3.0 | 3.5 | °C/W |
| Thermal Resistance ② | Junction to Case @ 125°C SMD-1 Package | | - | - | 2.0 | 2.5 | - | 2.0 | 2.5 | °C/W |

NOTES:

- (1) Output is decoupled to ground using 10µF low ESR tantalum capacitor unless otherwise specified.
- Quaranteed by design but not tested. Typical parameters are representative of actual device performance but are for reference only. Not applicable to post irradiation performance.
- (3) All output parameters are tested using a low duty cycle pulse to maintain TJ = TC.
- (4) Industrial grade and shall be tested to subgroup 1 unless otherwise specified.
- (5) Military grade devices ("H" and "K" suffix) shall be 100% tested to subgroups 1,2 and 3.
- 6 Subgroup 1 $TA = TC = +25^{\circ}C$ Subgroup 2 $TA = TC = +125^{\circ}C$
 - Subgroup 3 $TA = TC = -55^{\circ}C$
- The output current limit function provides protection from transient overloads but it may exceed the maximum continuous rating. Continuous operation in current limit may damage the device. For compliance with MIL-STD 833 revision C current density specifications, the MSK 5971RH is derated to 2 amps.
- (8) Continuous operation at or above absolute maximum ratings may adversely effect the device performance and/or life cycle.
- 9 Pre and Post irradiation limits at 25°C, up to 100Krad TID, are identical unless otherwise specified. Post irradiation performance is guaranteed by design with a 2X radiation design margin.
- (10) Reference DSCC SMD 5962R09212 for electrical specification for devices purchased as such.
- (11) Internal solder reflow temperature is 180°C, do not exceed.

A

MSK5971K/HRH.....--55°C to +125°C

APPLICATION NOTES

BYPASS CAPACITORS

Minimum requirement for output capacitance is $10\mu F$, low ESR tantalum. A $22\mu F$ low ESR (0.5-2 ohm) tantalum capacitor attached close to the regulator's output will ensure stability in most applications. This will effectively lower the regulator's output impedance, increase transient response and eliminate any oscillations that are normally associated with low dropout regulators. Additional bypass capacitors can be used at the remote load locations to further improve regulation. These can be either of the tantalum or the electrolytic variety. Unless the regulator is located very close to the power supply filter capacitor(s), a 4.7uF minimum low ESR (0.5-2 ohm) tantalum capacitor should also be added to the regulator's input. An electrolytic may also be substituted if desired. When substituting electrolytic in place of tantalum capacitors, a good rule of thumb to follow is to increase the size of the electrolytic by a factor of 10 over the tantalum value

LOAD REGULATION

With a three-terminal device, it is not possible to provide true remote load sensing. The resistance of the connection to the load will limit the load regulation of the device. The best load regulation will be achieved when the top of the divider is connected to the pin as close to the case of the MSK 5971RH as possible and the bottom of the divider as close to the load as possible. This will effectively reduce the effect of the parasitic line resistance on the load regulation of the device.

Refer to Figure 2 for proper connections.

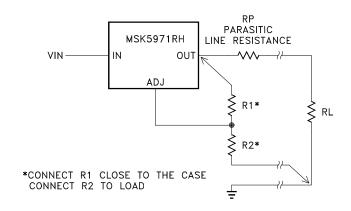


FIGURE 2

OUTPUT VOLTAGE

The MSK 5971RH develops a 1.250 Volt reference between the output pin and the adjust pin. A resistor, R1, is placed between the output and adjust pins, causing current to flow through R1 and down through R2, setting the output voltage. R1 may be chosen to sink the minimum required load current of 10mA. Refer to Figure 1 for connections and Vout calculation.

RIPPLE REJECTION

The typical curve for ripple rejection is shown with the adjust pin bypassed as shown in Figure 3. For proper bypassing and ripple rejection approaching the values of the curve, the impedance of the adjust pin capacitor at the ripple frequency should be less than the value of R1. The value of the adjust pin capacitor is a function of the input ripple frequency. For instance, if R1=100 Ω , at 120Hz the adjust pin capacitor should be 25µF. At 10KHz only 0.22uF is required.

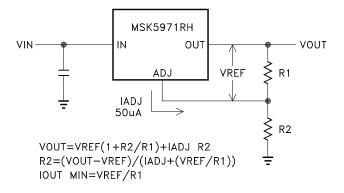
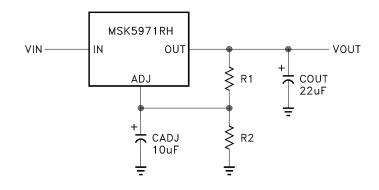


FIGURE 1 FIGURE 3



APPLICATION NOTES CONT'D

OVERLOAD SHUTDOWN

The regulator features both power and thermal overload protection. When the maximum power dissipation is not exceeded, the regulator will current limit slightly above it's 3 amp rating. As the Vin-Vout voltage increases, however, shutdown occurs in relation to the maximum power dissipation curve. If the device heats enough to exceed its rated die junction temperature due to excessive ambient temperature, improper heat sinking etc., the regulator also shuts down until an appropriate junction temperature is maintained. It should also be noted that in the case of an extreme overload, such as a sustained direct short, the device may not be able to recover. In these instances, the device must be shut off and power reapplied to eliminate the shutdown condition.

TOTAL DOSE RADIATION TEST PERFORMANCE

Radiation performance curves for TID testing will be generated for all radiation testing performed by MS Kennedy. These curves will show performance trends throughout the TID test process and can be located in the MSK 5971RH radiation test report. The complete radiation test report will be available in the RAD HARD PRODUCTS section on the MSK website.

HEAT SINKING

To determine if a heat sink is required for your application and if so, what type, refer to the thermal model and governing equation below.

Governing Equation: Tj = Pd x (R θ jc + R θ gcs + R θ sa) + Ta

WHERE

Ti = Junction Temperature

Pd = Total Power Dissipation

Rθjc = Junction to Case Thermal Resistance

Rθcs = Case to Heat Sink Thermal Resistance

Rθsa = Heat Sink to Ambient Thermal Resistance

Tc = Case Temperature

Ta = Ambient Temperature

Ts = Heat Sink Temperature

EXAMPLE:

This example demonstrates an analysis of the TO-257 package with the regulator operating at one-half of its maximum rated output current.

Conditions for MSK 5971RH:

$$VIN = +8.0V$$
; $Iout = +1.5A$

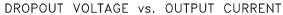
- 1) Assume 45° heat spreading model.
- 2) Find positive regulator power dissipation:

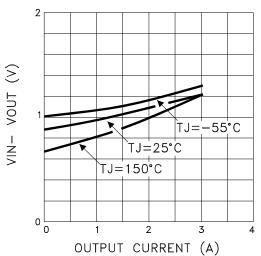
- For conservative design, set Tj = +125°C Max.
- 4) For this example, worst case Ta =+90°C.
- 5) R θ jc = 3.5°C/W from the Electrical Specification Table.
- 6) R θ cs= 0.15°C/W for most thermal greases.
- 7) Rearrange governing equation to solve for $R\theta$ sa:

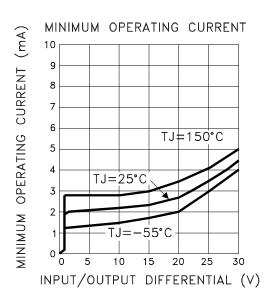
R
$$\theta$$
sa = ((Tj - Ta)/Pd) - (R θ jc) - (R θ cs)
= (125°C - 90°C)/4.5W - 3.5°C/W - 0.15°C/W
= 4.1°C/W

In this case the result is 4.1°C/W. Therefore, a heat sink with a thermal resistance of no more than 4.1°C/W must be used in this application to maintain regulator circuit junction temperature under 125°C.

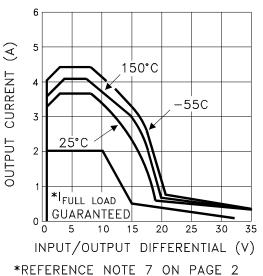
TYPICAL PERFORMANCE CURVES



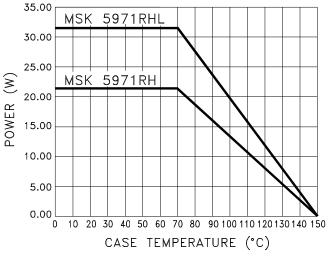


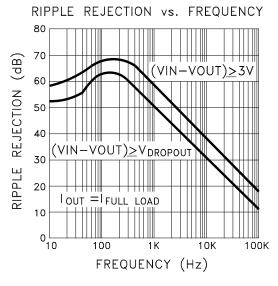


INTERNAL CURRENT LIMIT vs. VIN-VOUT

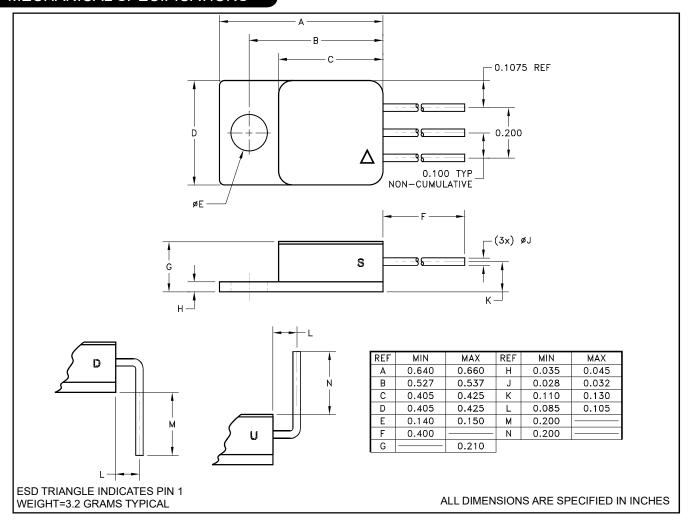


POWER DISSIPATION vs. TEMPERATURE

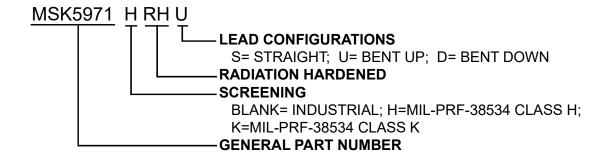




MECHANICAL SPECIFICATIONS



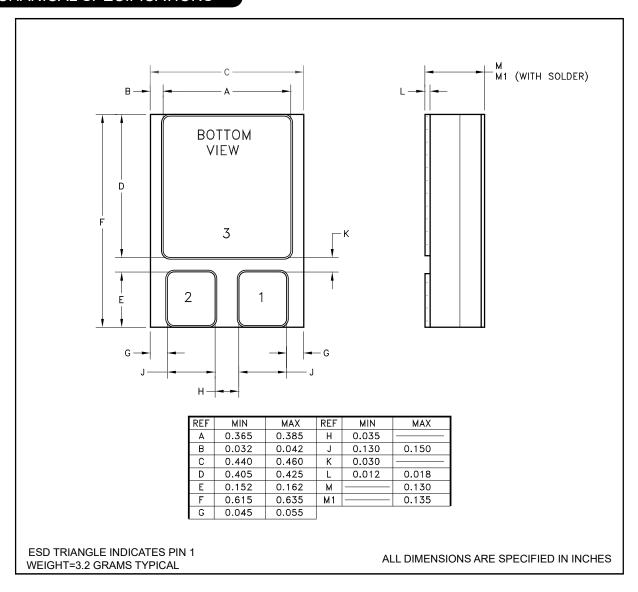
ORDERING INFORMATION



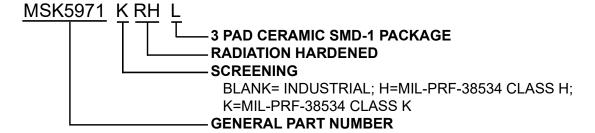
The above example is an adjustable Class K regulator with leads bent up.

Ordering information for the 3 pad ceramic SMD-1 package is contained on the next page.

NOTE: See DSCC SMD 5962R09212 for DSCC part number options.



ORDERING INFORMATION



The above example is an adjustable Class K regulator.

NOTE: See DSCC SMD 5962R09212 for DSCC part number options.

REVISION HISTORY

| REV | STATUS | DATE | DESCRIPTION |
|-----|----------|-------|---|
| Н | Released | 07/14 | Update format, assign form number and add internal solder note. |
| J | Released | 09/17 | Update parameters to better correlate to manufacturer's pre and post rad specs. |
| K | Released | 11/17 | Change output curent maximum to 3A |

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